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## Trip Computer

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TRIP COMPUTER  
FINAL REPORT  
SENIOR DESIGN  
PHASE II

April 27, 1990

Submitted by

Tim Braun

Submitted to

Professor Roger Hack, EET Department

Professor E.D. Devine, English Department

## ABSTRACT

The trip computer is designed to aid truck drivers by providing the driver with such vital information as the average and current speed of the vehicle, total distance travelled, and a time-to-distance conversion. The computer converts the rotational motion of the speedometer cable into vehicle speed in miles-per-hour.

The trip computer is based on an Intel 8052-AH BASIC microcontroller. The microcontroller has a full-feature BASIC interpreter built on the chip, this allows users to program in the simpler to use BASIC language.

Due to the versatility of this device, it could be easily adapted to any type of vehicle.

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